Applicant: John M. Amarai e. al.

Serial No.: 09/814,453 Filed: March 22, 2001

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Attached is a marked-up version of the changes being made by the current amendment.

Favorable examination and early passage to issue are respectfully requested.

Respectfully submitted,

Date: 1/4 27,2001

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## Version with markings to show changes made

## In the specification:

Paragraph beginning at page 6, line 2, has been amended as follows:

[Fig. 3 is] Fig. 3, comprised of Figs. 3a and 3b, is a low-level block diagram of circuitry contained in the MPEG receiver; and